

Docket No.: M0025.0305/P305
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Anthony J. Magrath

Application No.: Not Yet Assigned

Confirmation No.:

Filed: Concurrently Herewith

Art Unit: N/A

For: WORD LENGTH REDUCTION
CIRCUIT

Examiner: Not Yet Assigned

INFORMATION DISCLOSURE STATEMENT (IDS)

MS Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 CFR 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement accompanies the new patent application submitted herewith.

A full translation of the non-English language references is enclosed.

A copy of each reference on the PTO/SB/08 is attached.

In accordance with 37 CFR 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other

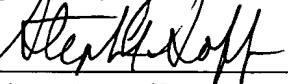
material information as defined in 37 CFR 1.56(a) exists. In accordance with 37 CFR 1.97(h), the filing of this Information Disclosure statement shall not be construed to be an admission that any patent, publication or other information referred to therein is "prior art" for this invention unless specifically designated as such.

It is submitted that the Information Disclosure Statement is in compliance with 37 CFR 1.98 and the Examiner is respectfully requested to consider the listed references.

The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 04-1073, under Order No. M0025.0305/P305. A duplicate copy of this paper is enclosed.

Dated: February 13, 2004

Respectfully submitted,

By 

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Substitute for form 1449A/B/PTO				Compleat if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Application Number	Not Yet Assigned
				Filing Date	Concurrently Herewith
				First Named Inventor	Anthony J. Magrath
				Art Unit	N/A
				Examiner Name	Not Yet Assigned
				Attorney Docket Number	M0025.0305/P305
Sheet	1	of	1		

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	AA	5,784,017	07-1998	Craven	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	CA	D. Reefman et al., "Signal processing for Direct Stream Digital", A Tutorial for Digital Sigma Delta Modulation and 1-Bit Digital Audio Processing, Version 1.0, 18 December 2002.	
	CB	"Architectures for Noise-Shaping Loop", 10.2, pp. 311-323	
	CC	M. Pedersen et al., "All Digital Power Amplifier Based on Pulse Width Modulation," Presented at the 96th AES Convention 1994.	
	CD	M. Sandler et al., "Ultra Low Distortion Digital Power Amplification," Presented at the 91st AES Convention 1991.	
	CE	C. Pascual et al., "Computationally Efficient Conversion from Pulse-Code Modulation to Naturally Sampled Pulse-Width Modulation," Presented at the 109th AES Convention, 2000.	
	CF	P. Craven, "Toward the 24-bit DAC: Novel Noise-Shaping Topologies Incorporating Correction for the Nonlinearity in a PWM Output Stage," <i>J. Audio Eng. Soc.</i> , Vol. 41, No. 5, May 1993, pp. 291-313.	
	CG	C. Tewksbury et al., "Oversampled, Linear Predictive and Noise-Shaping Coders of Order N>1", <i>IEEE Trans Circuits and Sys.</i> , Vol. CAS-25, pp. 436-447, July 1978.	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.